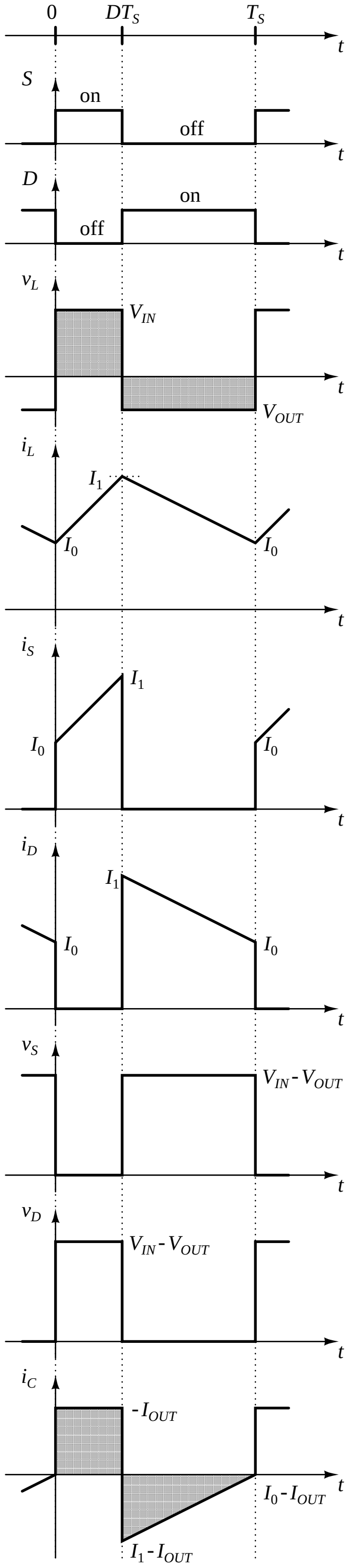
0
DT_S
T_S
t
S
on
off
t
D
on
off
t
v_L
V_{IN}
t
V_{OUT}
i_L
I_1
I_0
I_0
t
i_S
I_1
I_0
I_0
t
i_D
I_1
I_0
I_0
t
v_S
V_{IN} - V_{OUT}
t
v_D
V_{IN} - V_{OUT}
t
i_C
- I_{OUT}
t
I_0 - I_{OUT}
I_1 - I_{OUT}