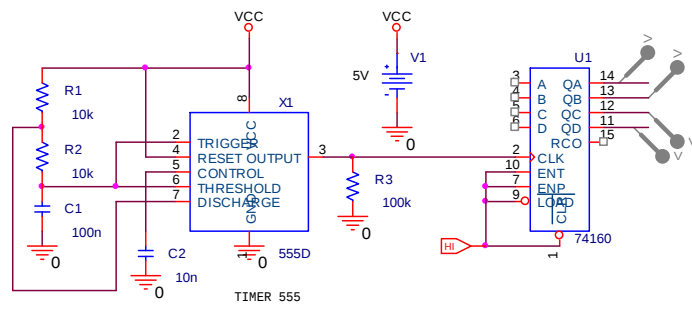
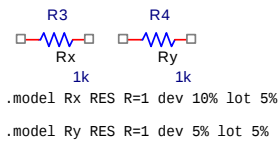
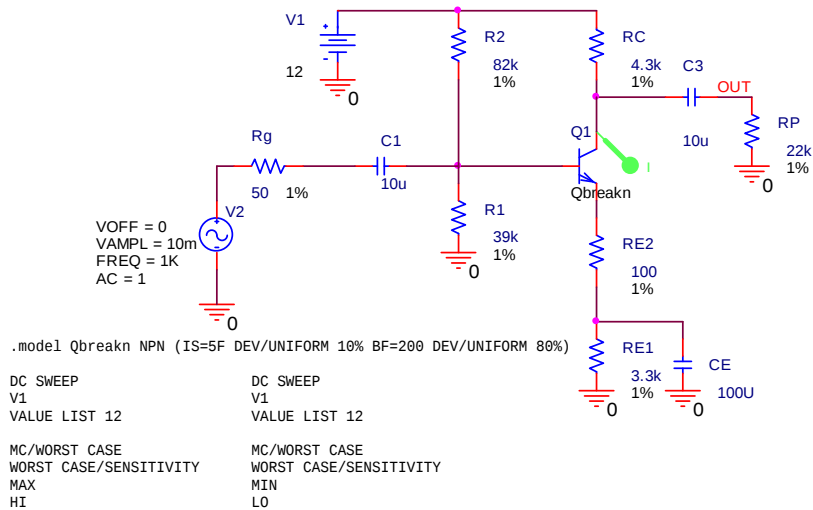




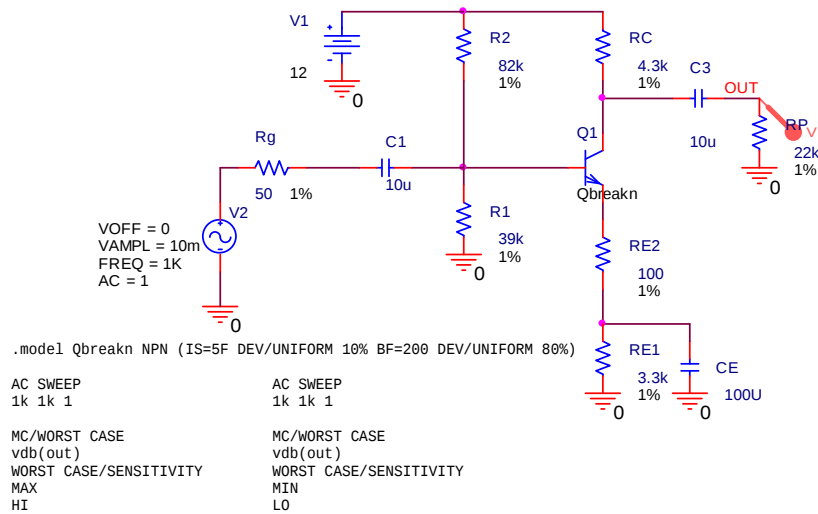
Timer 555 i binarni brojac: Tran 40m (+7442a)



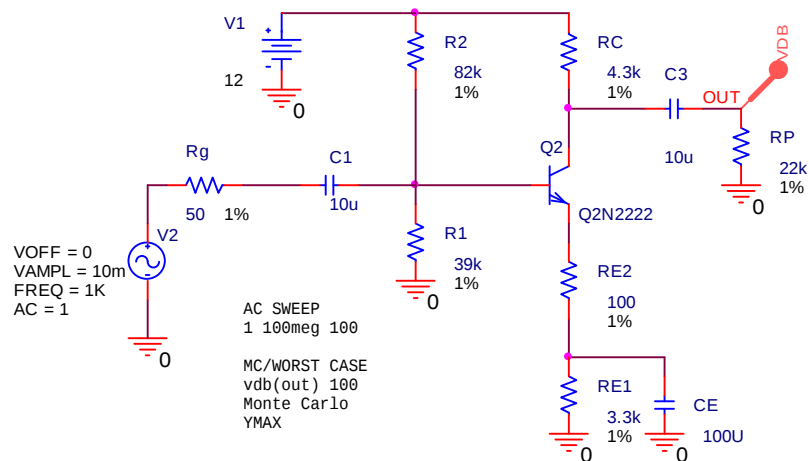
Model otpornika: zadavanje tolerancije



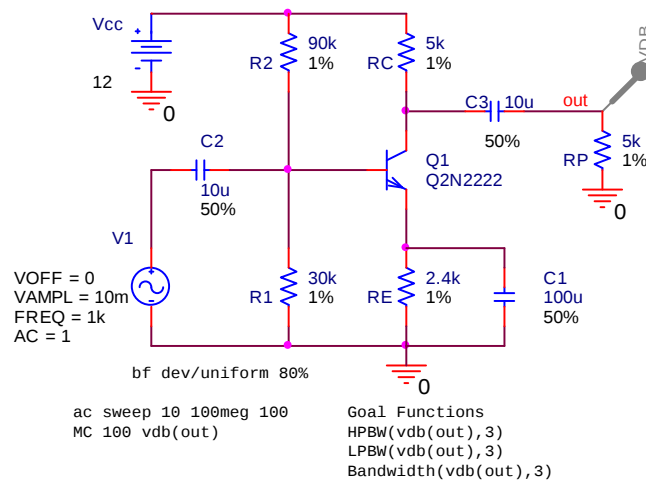
Worst case (min i max struja kolektora)



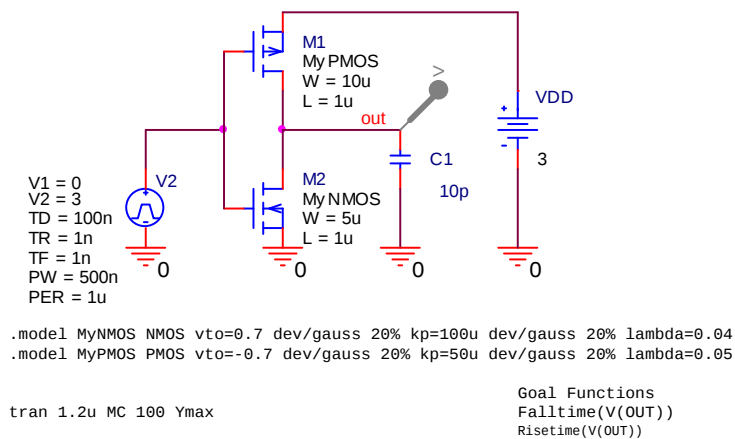
Worst case (min i max pojaćanje)



Frekvencijska karakteristika jednog pojacavaca



Performance Analysis – Amplifier Frequency Response



Performance Analysis - Inverter Switching Speed