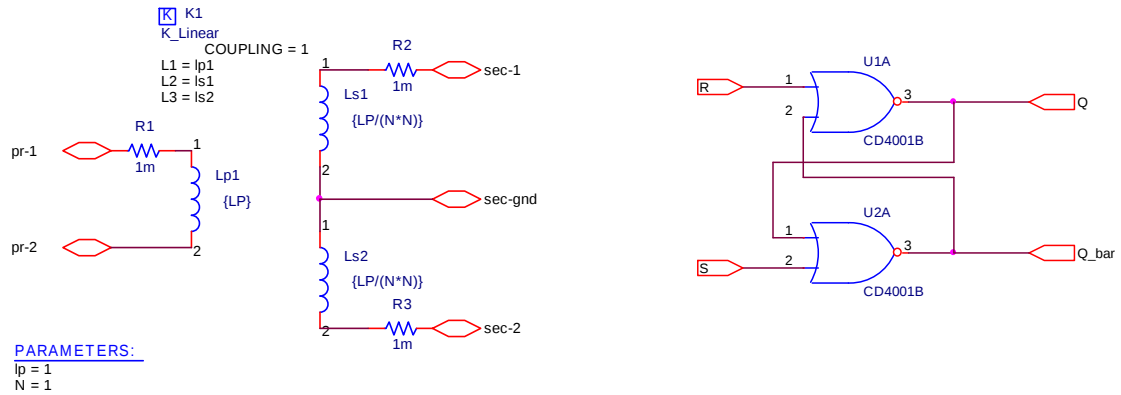
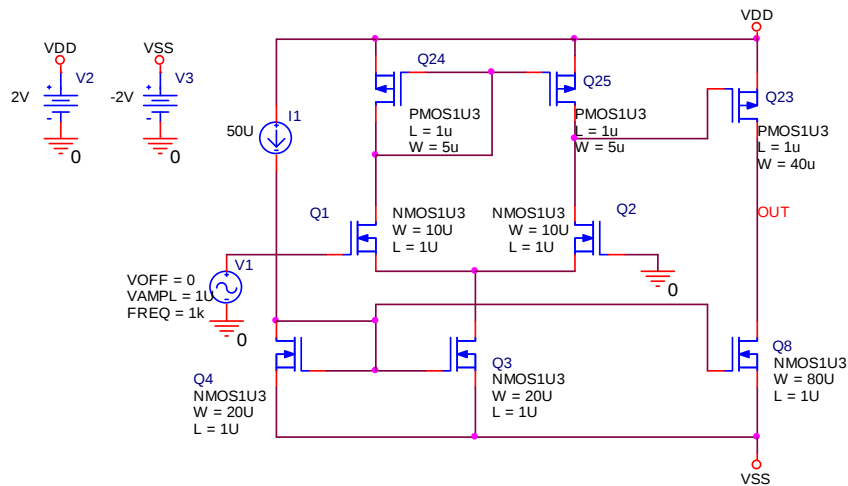


Create Subcircuit: 1. Operational Amplifier

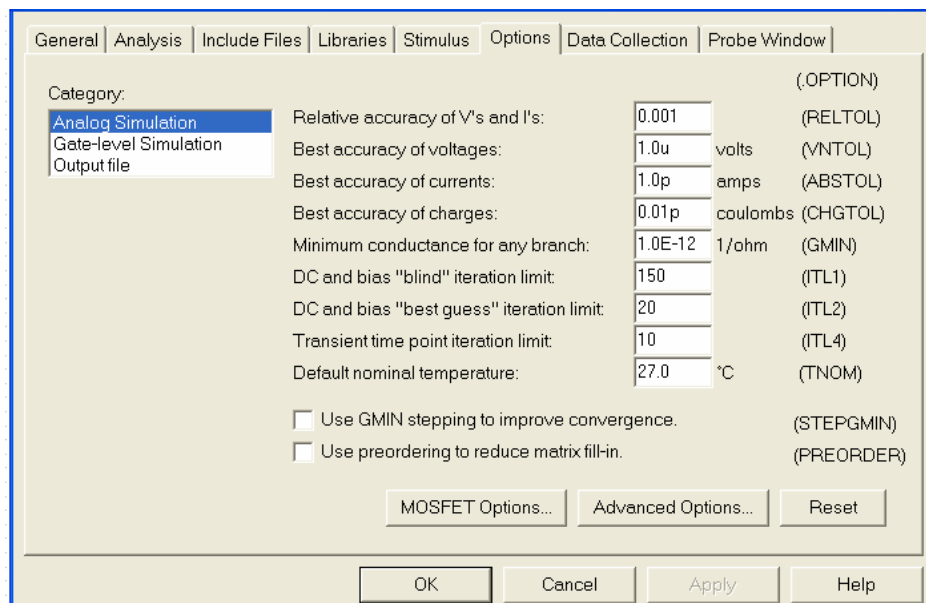


Create Subcircuit: 2. Transformer

3. SR Latch



CMOS diferencijalni pojačavac



Convergence Problems: Options