

Title: FPGA Engineer - multiple positions in Belgrade and Curpija

Ref. code: FPGA-E20

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. Electrical Engineer or equivalent degree from **Faculty of Electrical Engineering**

Job type: Permanent

The ideal candidate will have following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatics and Telecommunications
- 2+ years' hands-on FPGA hardware design experience
- Experience with Xilinx/Altera FPGA simulation and synthesis tools
- High speed digital design

The following skills will be a plus:

- Hardware/board design
- Verilog and VHDL experience
- Embedded processors

Responsibilities:

You will be part of the team working on the high-speed 100k+ gates FPGA design using VHDL and/or Verilog (Xilinx and/or Altera) with simulation using Cadence/Mentor simulators. The position requires a close working with the different hardware and software engineering teams; consequently, the candidate must have the knowledge and the interpersonal skills to be able to fit into very focused group of people working on very complex product development.

Job Positions Descriptions and Responsibilities:

All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

Please send your CV to jobs@hdl-dh.com, or fill in the online application form:

<http://www.hdl-dh.com/job-application.php>

Title: Verification Engineer – multiple positions in Belgrade and Cuprija

Ref. code: JSSV-E20

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. or equivalent degree from **Faculty of Electrical Engineering**

Job type: Permanent

The ideal candidate will have following skills and background:

1) Candidates without previous experience:

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatic, and Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Knowledge of C/C++

2) Candidates with previous experience:

- Extensive SoC design and verification experience using VHDL, Verilog, SystemVerilog (OVM/UVM) or Specman e
- In-depth knowledge of Testbench and CPU/MCU Architecture
- Experience of Assertion based verification and Formal methods
- In-depth knowledge of ARM System Architecture
- Highly motivated and dynamic engineer

The following skills will be a plus:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl).
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN)).
- Knowledge of C/C++ language and/or SystemC.
- Familiar with complex flows for System-on-Chip projects.

Responsibilities:

Candidate will be involved in all stages of complex SoC verification including specification, testbench design, verification plan development, verification execution and verification sign off.

Proactively collaborate with team members in different locations. Review verification environment architecture and implementation specifications.

Job Positions Descriptions and Responsibilities:

All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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ASIC Digital Engineer - multiple positions in Belgrade and Curpija

Ref. code: JDE-E20

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. or equivalent degree from **Faculty of Electrical Engineering**

Job type: Permanent

The ideal candidate will have following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatic or Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly

The following skills will be a plus:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl)
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN))
- Basic knowledge of CPU/MCU architecture

Job Positions Descriptions and Responsibilities:

All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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Title: Junior Analog Engineer - 6 positions in Belgrade and 2 positions in Cuprija

Ref. code: JAE-E20

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. or equivalent degree from **Faculty of Electrical Engineering**

Job type: Permanent

The ideal candidate will have following skills and background:

- M.Sc.EE or equivalent degree in Electronics, Computer Science
- Highly motivated and dynamic engineer able to adopt new technology quickly

The following skills will be a plus:

- Analog design experience
- Basic experience with state of the art analog design tools
- Experience with the implementation technologies below 90nm/CMOS and all steps in analog design flow

Responsibilities:

You will be part of the team working on the implementation of advanced analog functions layout, simulations and circuit design will be part of your daily tasks.

Job Positions Descriptions and Responsibilities:

All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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Title: **SoC Physical Implementation Engineer – multiple positions in Belgrade and Cuprija**
Ref.code: **SPiE-SoC1**

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. or equivalent degree from **Faculty of Electrical Engineering**

Job type: Permanent

The ideal candidate should have the following skills and background:

1)Candidates without previous experience:

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatic or Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Proficient Linux/Unix user
- Fluent English is a must

2)Candidates with previous experience:

- Extensive digital physical design experience using Cadence EDI tools or similar
- In-depth knowledge of physical implementation of digital integrated circuits (synthesis, LEC, DFT, ATPG, floor-planning, place&route, CTS, timing closure, LVS/DRC, etc.)
- In-depth knowledge of physical implementation tools (Cadence Encounter RTL Compiler, Cadence Encounter Digital Implementation, Cadence Encounter Test, Synopsys Design Compiler, Synopsys TetraMAX, etc.)
- Highly motivated and dynamic engineer
- Proficient Unix user
- Fluent English is a must

The following skills will be a plus:

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl, Tcl)
- Understanding of analog electronics and physical processes in silicon chips
- Familiar with complex flows for SoC project

Responsibilities:

1) Candidates without previous experience:

Candidate will receive comprehensive trainings for back-end design of System-on-Chip (SoC). Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification.

2) Candidates with previous experience:

Candidate will be involved in all stages of complex Soc/ASIC design including front-end design, physical design implementation, timing closure and sign off.

Proactively collaborate with team members in different locations and prepare a silicon chip for the fabrication.

This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary.

Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

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Title: **Junior System Administrator - 1 position**

Ref.code: **JIT-E1**

Location: Belgrade, Serbia

Job type: Permanent

The ideal candidates should have the following skills and background:

Candidates should have 1 to 2 years of experience as a Unix/Linux system Admin, more experience would be considered as plus :

- Bachelor's degree/MS in Computer Science, Telecommunication or related discipline
- Proficient Linux/Unix user
- Fluent English is a must
- PC architecture in-depth knowledge
- Knowledge of Linux Server Administration is must
- Spoken and written English
- Understanding of PHP and HTML
- Experience scripting and automating sysadmin tools with shell scripts
- Additional technical and/or business certifications are an asset
- Team player

Responsibilities:

Candidates with 1 to 2 years of experience as a Unix/Linux system Admin will be member of HDL Design House IT team and will be responsible for the following tasks:

- 1st line service desk support: case logging, resolution and troubleshooting
- Hardware maintenance and inventory
- Assistance in the configuration of software, IT systems
- Assistance in training of internal staff on IT systems
- Assistance in composition of marketing material
- Installing Linux workstations
- Basic server system administration (creating mail and user accounts, DHCP,DNS and maintenance of company related applications)
- Basic Windows server administration
- Performs miscellaneous job-related duties as assigned

***Scope of responsibilities for this particular opportunity may change and will include, but not be limited to the above mentioned.**

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