

**Title: FPGA Engineer - 4 positions**

Ref. code: FE-E2

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. Electrical Engineer

**The ideal candidate will have following skills and background:**

- M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering
- 2+ years' hands-on FPGA hardware design experience
- Experience with Xilinx/Altera FPGA simulation and synthesis tools
- High speed digital design

**The following skills will be a plus:**

- Hardware/board design
- Verilog and VHDL experience
- C/C++
- Embedded processors

**Responsibilities:**

You will be part of the team working on the High-speed 100k+ gates FPGA design using VHDL and/or Verilog (Xilinx and/or Altera) with simulation using Cadence/Mentor simulators. The position requires a close working with the different hardware and software engineering teams; consequently, the candidate must have the knowledge and the interpersonal skills to be able to fit into very focused group of people working on very complex product development.

**Job Positions Descriptions and Responsibilities:**

**All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!**

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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**Please send your CV to [jobs@hdl-dh.com](mailto:jobs@hdl-dh.com), or fill in the online application form:**

**<http://www.hdl-dh.com/job-application.php>**

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**Title: Verification Engineer – multiple positions in Belgrade and Cuprija**

Ref. code: JSSV-E10

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. or equivalent degree from **School of Electrical Engineering**

**The ideal candidate will have following skills and background:**

**1) Candidates without previous experience:**

- M.Sc.EE or equivalent degree in Electronics, Informatics, Computer Science, Automatic or Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly
- Knowledge of C/C++

**2) Candidates with previous experience:**

- Extensive SoC design and verification experience using VHDL, Verilog, SystemVerilog (OVM/UVM) or Specman e
- In-depth knowledge of Testbench and CPU/MCU Architecture
- Experience of Assertion based verification and Formal methods
- In-depth knowledge of ARM System Architecture
- Highly motivated and dynamic engineer

**The following skills will be a plus:**

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl).
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN).
- Knowledge of C/C++ language and/or SystemC.
- Familiar with complex flows for System-on-Chip projects.

**Responsibilities:**

Candidate will be involved in all stages of complex SoC verification including specification, testbench design, verification plan development, verification execution and verification sign off.

Proactively collaborate with team members in different locations. Review verification environment architecture and implementation specifications.

**Job Positions Descriptions and Responsibilities:**

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For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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**Junior ASIC Digital Engineer - 4 positions in Belgrade and 3 positions in Cuprija**

Ref. code: JDE-E7

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. Electrical Engineer

**The ideal candidate will have following skills and background:**

- M.Sc.EE or equivalent degree in Electronics, Computer Science, Automatic or Telecommunications
- Highly motivated and dynamic engineer able to adopt new technology quickly

**The following skills will be a plus:**

- Familiar with scripting tools and languages (e.g. bash, csh, awk, Perl)
- Familiar with software/hardware development tools (e.g. make and versioning tools (e.g. CVS/SVN))
- Knowledge of C/C++ language

**Job Positions Descriptions and Responsibilities:**

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Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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**Title: Junior Analog Engineer - 6 positions in Belgrade and 1 position in Cuprija**

Ref. code: JAE-E7

Location: Belgrade and Cuprija, Serbia, Europe

Education: M.Sc. Electrical Engineer

**The ideal candidate will have following skills and background:**

- M.Sc.EE or equivalent degree in Electronics, Computer Science or Electrical engineering
- Highly motivated and dynamic engineer able to adopt new technology quickly

**The following skills will be a plus:**

- Analog design experience
- Experience with state of the art analog design tools
- Experience with the implementation technologies below 90nm/CMOS and all steps in analog design flow

**Responsibilities:**

You will be part of the team working on the implementation of advanced analog functions Layout, simulations and circuit design will be part of your daily tasks.

**Job Positions Descriptions and Responsibilities:**

**All open positions are for permanent employment. For all positions HDL Design House offers competitive salary (plus additional benefits)!**

Junior candidates will receive comprehensive trainings from experts for design/analog/verification of ASIC/FPGA System-on-Chip. Successful training completion and testing will be condition for candidates to join larger teams working in all stages of complex SoC design and verification. This is a unique opportunity to join a rapidly expanding company offering fantastic career and skills development opportunities, as well as an exceptional salary. Engineers with no prior experience are also desirable, making this the ideal opportunity for a recent graduate engineer to fast-track their career development into a more senior role.

For all candidates proficient knowledge of UNIX/Linux and fluent English is must prerequisite!

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