

KATEDRA ZA ELEKTRONIKU

Laboratorijske vežbe

ELEMENTI ELEKTRONIKE

Vežba br. 5

KOMPARATORI I LOGIČKA KOLA

Datum: _____

Vreme: _____ – _____

Studenti:

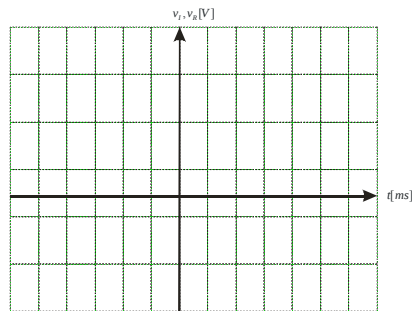
1. _____ grupa _____
2. _____ grupa _____

Dežurni: _____

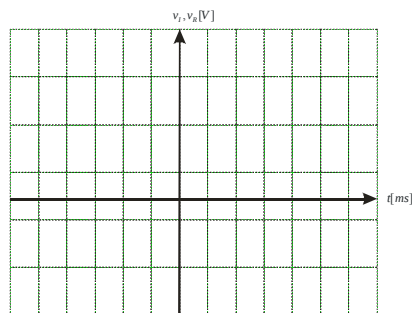
Ocena: _____

2.1. Komparator LM339

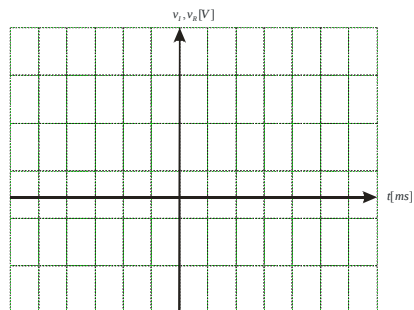
1) $v_R = 2\text{ V}$, $V_m = 1\text{ V}$



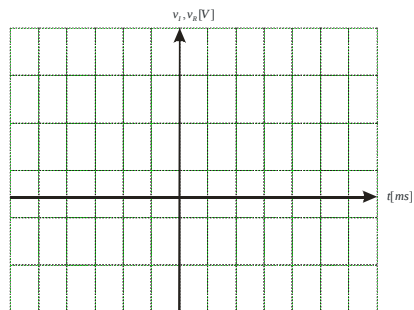
2) $v_R = 2\text{ V}$, $V_m = 3\text{ V}$



3) $v_R = -2\text{ V}$, $V_m = 1\text{ V}$

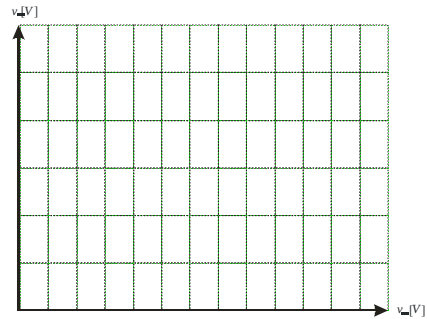


4) $v_R = -2\text{ V}$, $V_m = 3\text{ V}$



2.2. CMOS NI logičko kolo

$$v_I = v_I(v_U)$$



$$V_{IL} =$$

$$V_{IH} =$$

$$V_{OL} =$$

$$V_{OH} =$$

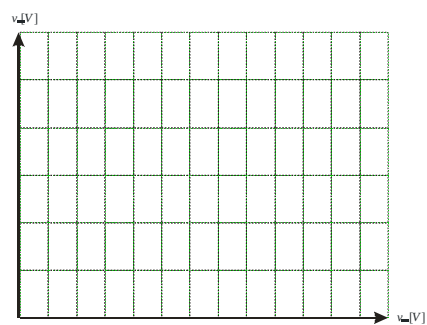
Margina šuma logičke jedinice =

Margina šuma logičke nule =

2.3. Šmitov komparator realizovan pomoću CMOS logičkih NI kola

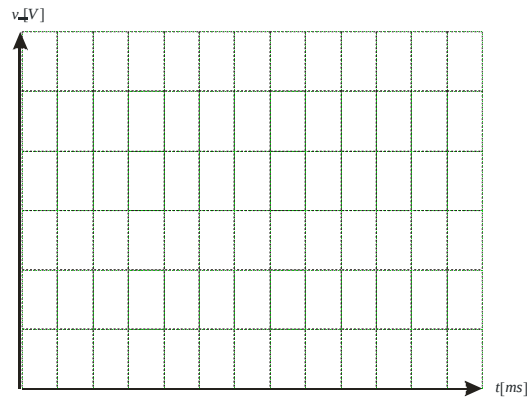
2.3.1.

$$v_I = v_I(v_U)$$



2.3.2.

$$v_I = v_I(t)$$



$$v_U = v_U(t)$$

